CP/M vers 2.2 Cold Start Loader.

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The following routines will boot CP/M from the Disk Jockey 2D (all revs and models), or from the Disk Jockey Hard disk controller.

The cold boot loader (sector 1, track 0) is loaded into the ram of the controller by the cold boot routine of the firmware. The first thing the boot does is to load into the controller ram, a version of the Disk Jockey 2D firmware. From then on, all calls to the firmware will actually be directed to the Disk Jockey Ram. The next process is to load in a boot routine which can load in all of CP/M. This is done by determining the size of the sectors on track 1, and using this information to load in the proper boot into 80H.

The following tables explain the order of sector loading for each of the different sector sizes. An entry of ----- represents a wrap back around (negative DMA adjustment). An entry flagged with astricks represents a partial sector load.

All sector sizes:

rack	Ø	sector 1	e7ØØ
	Ø	8	2cØØh
	Ø	10	2døøh
	Ø	12	2eØØh
	Ø	14	2føøh
	Ø	16	3000h
	Ø	18	2789h
	Ø	2Ø	2880h
	Ø	22	298Øh
	Ø	24	2a8Øh
	Ø	26	2b89h
	Ø	9	2c8Øh
	Ø	11	2d89h
	Ø	13	2e3Øh
	Ø	15	2f8Øh
	Ø	17	27ØØh
	Ø	19	28øøh
	Ø	21	2900h
	Ø	23	2aØØh
	Ø	25	2b@@h

The following depend on the sector size, all sectors are from track 1.

r	256		512			1024	
•	sec	address	sec	address		sec	address
•	1	2c00h	1	2c00h		1	2cØØh
•	3	2e00h	3	3000h		3	3400h
•	5	3000h	5	3400h		5	3cØØh
•	7	3200h	7	38ØØh	**	7	44ØØh
•	9	3490h	9	3c00h			
:	11	36ØØh	11	4000h		2	3ØØØh
•	13	3800h	13	4400h		4	3800h
•	15	3aIIh				6	4000h
•	17	3c00h	2	2eØØh		Ü	10001
:	19	3eØØh	4	3200h			
•	21	4000h	6	3600h			

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1/16/82 Changes shows

```
* 23
             4200h
                             8
                                      3a00h
      25
             4400h
                             10
                                      3eØØh
                             12
                                      4200h
      2
             2dØØh
    * 4
             2fØØh
    * 6
             31ØØh
                             Discus MlØ, M2Ø, M26
    * 8
             33ØØh
                                      e400h (If 2D is present)
    * 10
             3500h
                             4
                                      e600h (If 2D is present)
    * 12
             37ØØh
                          ** 20
                                      45ØØh
    * 14
             3900h
                                      2700h
    * 16
                             6
             3bØØh
                                      2900h
    * 18
             3døøh
                             7
                                      2bØØh
    * 2Ø
            3fØØh
                             8
                                      2d00h
    * 22
            4100h
                             9
                                      2fØØh
    * 24
                             10
             4300h
                                      31ØØh
      26
             4500h
                             11
                                      3300h
                             12
                                      35ØØh
                             13
                                      37ØØh
                             14
                                      39ØØh
                             15
                                      3bØØh
                             16
                                      3dØØh
                             17
                                      3fØØh
                             18
                                      4100h
                              19
                                      4300h
             title
                     '*** Cold Boot Loader for CP/M Ver. 2.2 ***'
                                                                                             - Aga 1/16/82
                     28 56
    msize
            equ
                                      ; Memory size of target CP/M
9000 bias
             equ
                     (msize-20)*1024 ; Memory offset from 20k system
Bloo ccp
                     2700h+bias
                                      ;Console command processor
             equ
¿Deo bios
             equ
                     ccp+1600h
                                      ;CBIOS address
    retries equ
                     10
                                      ;Maximum # of disk retries
      The following equates set up the relationship between the
      2D floppies and the Hard Disk Controllers.
                                                                                                ADO 1/16/82
    first
                                      ;\emptyset = Floppies are A-D drives and
            equ
                                              Hard Disk are E-P
                                      ;1 = Hard Disks are A-L'drives and
                                              Floppies are M-P
    maxhd
                                      ; Set to number of hard disks
            equ
    maxflop equ
                                      ;Set to number of floppies
                                                                                                        4. LUDKS LIKE ALL 15 TESTED
                                                                                                15 "ZERO" OR NOT ERVALTO ZERO
      The following equates are for the Diskus Hard disk if wanted.
                     (maxhd ne \emptyset) and first ; Want Hard Disk included ?
             if
    hdorg
             equ
                     5Øh
                                      ; Hard Disk Controller
    hdstat equ
                     hdorg
                                      ; Hard Disk Status
    hdcntl
            equ
                     hdorg
                                      ; Hard Disk Control
    hddata
            equ
                     hdorg+3
                                      ;Hard Disk Data
    hdfunc
            equ
                     hdorg+2
                                      ; Hard Disk Function
    hdcmnd
            equ
                     hdorg+1
                                      ; Hard Disk Command
    hdreslt equ
                     hdorg+1
                                      ; Hard Disk Result
    retry
             equ
                                      ; Retry bit of result
    tkz
                     1
             equ
                                      ;Track zero bit of status
    opdone equ
                                      *Operaction done bit of status
```

```
; Complete bit of status :
complt
       equ
                4
tmout
                8.
                                 ;Time out bit of status
        equ
wfault
                10h
                                 ;Write fault bit of status
        equ
                20h
                                 ;Drive ready bit of status
drvrdy
        equ
indx
        equ
                40h
                                 ; Index bit of status
pstep
                4
                                 ;Step bit of function
        equ
                Øfbh
                                 ;Step bit mask of function
nstep
        equ
hdrlen
        equ
                4
                                 ;Sector header length
                                 ; Sector data length
                512
secln
        equ
                                 ;Write enable
wenabl
        equ
                Øfh
                                 ;Write reset of function
wreset
        equ
                Øbh
scenbl
                5
                                 ;Controller control
        equ
                7
                                 ; Disk clock for control
dskclk
        equ
                                 ;Direction mask for function
                Øf7h
mdir
        equ
                Øfch
                                 :Null command
null
        equ
idbuff
        equ
                Ø
                                 ;Initialize data command
isbuff
        equ
                8
                                 ; Initialize header command
rsect
                1
                                 ; Read sector command
        equ
                5
                                 ;Write sector command
wsect
        equ
        endif
* Cold Boot loader common to all sector sizes.
* This sector is loaded into memory at e700h in a standard
* configuration. It is responsible for reading most of track 0
* into memory on cold boots.
        if
                 maxflop ne Ø
origin
        equ
                 ØEØØØH-
djram
        equ
                 origin+400h
djboot
                djram
        equ
        endif
        if
                 (maxflop ne 0)
offsetb equ
                 900h-origin
        else
                 Ø
offsetb equ
        endif
        if
                 not first
putden equ
                 origin+02dh
                                 ;Set density routine on Disk Jockey 2D
putdma
        equ
                 origin+12h
                                 ; Disk Jockey 2D set DMA address routine
                 origin+27h
                                 ;Disk status routine on Disk Jockey 2D
getstat equ
                                 ; Disk Jockey 2D set sector routine
putsec equ
                 origin+0fH
                 origin+Øch
                                 ;Disk Jockey 2D set track routine
puttrk equ
                                 ;Disk Jockey 2D track Ø seek
puthom
        equ
                 origin+9h
doread equ
                 origin+15h
                                 ;Disk Jockey 2D read routine
                                 ; Disk Jockey 2D flash error light routine
boterr equ
                 origin+2ah
                 origin
        org
                                 ;Disk Jockey 2D ram
diff
        set
                 origin+700h-$
                                 ;Offset to boot loader address
        lxi
                 sp, stac+diff
firmlod mvi
                 a,6
                                 ;Previous sector #
newsec
       equ
                 $-1
        inr
                 a
                                 ;Update sector #
        inr
        cpi
                 27
                                 ;Test if all done
        jz
                 ccp+500h
        jc
                 nowrap+diff
                                 ;Test if wrap around
        sui
                 19
                 newsec+diff
                                 ·Save the undated sector #
nowran
        eta
```

```
mov
                c,a
        call
                putsec
                                ;Set up the sector
        lxi
                h,ccp+400h
                                ; Previous DMA address
newdma
        equ
                $-2
                d,100h
        lxi
                                ;Update DMA address
        dad
                đ
        mov
                a,h
        cpi
                (ccp+980h)/100h
                nowrp+diff
        ic
        jnz
                wrp+diff
        mov
                a,1
        cpi
                (ccp+980h) mod 100h
                nowrp+diff
        jc
wrp
        lxi
                d,-980h
        dad
nowrp
        shld
                newdma+diff
                                ;Save the updated DMA address
        mov
                b,h
        mov
                c,1
        call
                putdma
                                ;Set up the new DMA address
                b.retries*100h+0; Maximum # of errors
        lxi
fread
        push
        call
                puttrk
                                ;Set up the proper track
        call
                doread
                                ; Read the sector
        pop
                firmlod+diff
                                ;Continue if no error
        jnc
        dcr
        jnz
                fread+diff
                                ;Keep trying if error
        qmŗ
                boterr
                                ; To many errors, flash the light
                80h-($ mod 30h)
        ds
        equ
stac
* The following equates relate to the Thinker Toys 2D controller*
* If the controller is non standard (ØEØØØH) only the ORIGIN
  equate need be changed. This version of the Cbios will work
  with 2D controller boards rev 0, 1, 3, 3.1, 4.
djcin
        equ
                djram+3h
                                 ;Disk Jockey 2D character input routine
dicout equ
                djram+6h
                                 ; Disk Jockey 2D character output routine
djhome equ
                djram+9h
                                 ;Disk Jockey 2D track zero seek
djtrk
                                ;Disk Jockey 2D track seek routine
        equ
                djram+Øch
djsec
                djram+0fh
                                ;Disk Jockey 2D set sector routine
        equ
djdma
        equ
                djram+012h
                                ;Disk Jockey 2D set DMA address
djread equ
                djram+15h
                                ;Disk Jockey 2D read routine
djwrite equ
                djram+18h
                                ; Disk Jockey 2D write routine
djsel
                                 ;Disk Jockey 2D select drive routine
        equ
                djram+lbh
djdmast equ
                djram+24h
                                ;Disk Jockey 2D dma status
djstat equ
                djram+27h
                                ;Disk Jockey 2D status routine
                                 ;Disk Jockey 2D error, blink led
djerr
                djram+2ah
       equ
djden equ
                djram+2dh
                                 ;Disk Jockey 2D set density routine
                                 ; Disk Jockey 2D terminal status routine
ditstat equ
                djram+21h
djside equ
                djram+30h
                                 ;Disk Jockey 2D set side routine
* The following three sectors of code reside at 80H. There is
* one sector for each of the possible sector sizes (256,512,
* 1024). Each sector is responsible for performing a Cold Boot
* for the specified sector size.
```

diff set 80h-\$

```
lxi
                sp,cstk256+diff ;Set up stack at end of this sector
        lxi
                b, 26*100h+1
                                ;B = sector count, C = sector #
clod256 push
                                ;Save sector and count
                b
        call
                djsec
                                ; Set the next sector to read
        lxi
                h,ccp+300h
                                ;Get DMA address (self modifying)
cdma256 equ
                $-2
                                ;Storage for previous DMA address
        lxi
                d,200h
                                 ;Offset to new DMA address
        dad
                                ; Add in offset, HL = new DMA address
        shld
                cdma256+diff
                                ;Save new DMA address
                                :Put DMA address into BC
        mov
                b,h
        mov
                c,l
        call
                djdma
                                 ;Set the DMA address
        call
                crd256+diff
                                 ;Attempt a read
        pop
                                 ; Recover sector number and count
                                         B = count, C = number
                                 ; Update sector count
        dcr
                b
        jz
                bios
                                 ;All done ?
        mvi
                a,2
                                 ; Sector update
        add
                C
                                 ; Add in the sector skew factor
                                 ; Put new sector back into C
        mov
                c,a
        cpi
                                 ; Past the end of the track?
        jс
                clod256+diff
                                 ; Take jump if not past end of track
        sui
                                 ; Perform a negative sector adjustment
        mov
                                 ; Put new sector in C
                c,a
        lxi
                h,ccp+400h
                                 ; Negative DMA adjustment
        shld
                cdma256+diff
                                 ; Save the new DMA address
        qmj
                clod256+diff
                                 ;Continue reading
 Crd256 does the actual read from the controller, the DMA
 address and sector # have already been set up.
crd256 lxi
                b, retries*100h+1 ; Maximum # of attempts
cr256
        push
                                ;Save error count
        call
                ditrk
                                 :Initialize the track
        call
                djread
                                 ;Attempt the read
                                 ; Restore the error count
        pop
        rnc
                                 ;Return if no error
        dcr
                                 ; Update error count
        jnz
                cr256+diff
                                 ;Try again if not to many errors
        jmp
                djerr
                                 ; Go and flash the light on controller
        ds
                80h-($ mod 80h)
cstk256 equ
* The next loads CP/M from a 512 byte sector diskette.
diff
        set
                80h-$
        lxi
                sp,cstk512+diff ;Set up stack at end of this sector
        lxi
                b,13*100h+1
                              ;B = sector count, C = sector #
clod512 push
                b
                                 ;Save sector and count
        call
                djsec
                                 ; Set the next sector to read
        lxi
                h,ccp+100h
                                 ;Get DMA address (self modifying)
cdma512 equ
                $-2
                                 ;Storage for previous DMA address
        lxi
                d,400h
                                 ;Offset to new DMA address
        dad
                d
                                 ; Add in offset, HL = new DMA address
        shld
                cdma512+diff
```

:Save new DMA address

```
b,h
        mov
                                 ; Put DMA address into BC
        mov
                 c,l
        call
                 didma
                                 ;Set the DMA address
        call
                 crd512+diff
                                 ;Attempt a read
        pop
                                 ; Recover sector number and count
                                         B = count, C = number
        dcr
                 b
                                 ; Update sector count
        jz
                bios
                                 ;All done ?
        mvi
                a,2
                                 ; Sector update
        add
                                 ; Add in the sector skew factor
        mov
                c,a
                                 ; Put new sector back into C
                                 ; Past the end of the track ?
        cpi
                14
        jс
                 clod512+diff
                                 ; Take jump if not past end of track
        sui
                13
                                 ; Perform a negative sector adjustment
        mov
                c,a
                                 ; Put new sector in C
        lxi
                h,ccp+300h
                                 ; Negative DMA adjustment
        shld
                cdma512+diff
                                 ; Save the new DMA address
        jmp
                 clod512+diff
                                 ;Continue reading
 Crd512 does the actual read from the controller, the DMA
  address and sector # have already been set up.
crd512 1xi
                b,retries*100h+1 ;Maximum # of attempts
cr512
        push
                b
                                 ;Save error count
        call
                djtrk
                                 ; Initialize the track
        call
                djread
                                 ;Attempt the read
        pop
                b
                                 ; Restore the error count
        rnc
                                 ;Return if no error
        dcr
                b
                                 ; Update error count
                cr512+diff
        jnz
                                 ;Try again if not to many errors
        jmp
                djerr
                                 ;Go and flash the light on controller
        ds
                80h-($ mod 80h)
cstk512 equ
* The next sector loads CP/M from a 1024 byte sector diskette.
diff
        set
                80h-$
        lxi
                sp,cstkl24+diff ;Set up stack at end of this sector
        lxi
                b,1*100h+7
                                 ;B = sector count, C = sector #
        call
                clod124+diff
                                 ;Load sector 5 into CCP
        1xi
                h,ccp+ldØØh
                                 ;Destination of move
        1xi
                d,ccp+500h
                                 ;Source of move
        lxi
                b, 200h
cmov124 ldax
                                 ;Get a byte of source
        mov
                m.a
                                 ; Move it
        inx
                h
                                 ;Bump destination
        inx
                                 ;Bump source
        dcr
                                 ;All done with this page ?
        jnz
                cmov124+diff
        dcr
        jnz
                cmov124+diff
        lxi
                h,ccp-300h
                                 ;Initial DMA address
        shld
                cdma124+diff
        lxi
                b,6*100h+1
                                 ;B = sector count, C = sector #
        call
                clod124+diff
        omi
                bios
```

```
clod124 push
                                 ;Save sector and count
                b
                                 ; Set the next sector to read
        call
                djsec
        lxi
                                 ; Get DMA address (self modifying)
                h,ccp-300h
                                 ;Storage for previous DMA address
cdma124 equ
                $-2
                d,800h
        lxi
                                 ;Offset to new DMA address
                                 ; Add in offset, HL = new DMA address
        dad
        shld
                cdma124+diff
                                 ;Save new DMA address
                                 :Put DMA address into BC
        mov
                b,h
                c,1
        mov
                                 :Set the DMA address
        call
                didma
        call
                crd124+diff
                                 ;Attempt a read
                                 ; Recover sector number and count
        pop
                b
                                         B = count, C = number
                                 :Update sector count
        dcr
                b
                                 :All done ?
        rz
                                 ; Sector update
        mvi
                a,2
        add
                C
                                 ;Add in the sector skew factor
                                 ; Put new sector back into C
        mov
                c,a
                                 ; Past the end of the track ?
        cpi
                                 :Take jump if not past end of track
                clod124+diff
        ic
                                 ; Perform a negative sector adjustment
        sui
        mov
                c,a
                                 ; Put new sector in C
        lxi
                h,ccp+100h
                                 ; Negative DMA adjustment
                                 ; Save the new DMA address
        shld
                cdma124+diff
                clod124+diff
                                 ;Continue reading
        jmp
* Rd124 does the actual read from the controller, the DMA
  address and sector # have already been set up.
                b, retries*100h+1 ; Maximum # of attempts
crd124
        lxi
crl24
        push
                b
                                 ;Save error count
                                 ; Initialize the track
        call
                djtrk
        call
                 djread
                                 ;Attempt the read
                                 ; Restore the error count
        pop
                b
        rnc
                                 :Return if no error
        dcr
                                 ; Update error count
                 crl24+diff
                                 ;Try again if not to many errors
        jnz
                                 :Go and flash the light on controller
        qmr
                djerr
                 80h-($ mod 80h)
        ds
cstkl24 equ
  The next three sectors of code also reside at 80H. There is
  one sector for each of the possible sector sizes (256,512,
* 1024). Each sector is responsible for performing a WARM Boot
  for the specified sector size.
  The following table shows how sectors are read in, skewing
  of the sectors is necessary because sequential sectors can
  not be read without waiting one complete revolution between
  each one. Entries of ---- represent a wrap around (negative
* DMA adjustment). An entry flagged with ** represents only a
  partial load from that sector.
  256
                         512
                                                  1024
  sec
        address
                         sec
                                 address
                                                  sec
                                                           address
  1
        3100h
                                 3100h
                                                           3100h
                         1
                                                  1
  3
                         3
                                 35ØØh
                                                           39ØØh
        33ØØh
                                                  3
  5
        3500h
                         5
                                  3900h
                                                  5
                                                           4100h
```

3dggh

3700h

```
** 9
* 9
                                                  2
                                                           35ØØh
        3900h
                                 4100h
* 11
        3bØØh
                                                  4
                                                           3dØØh
* 13
        3dØØh
                                 33ØØh
                         2
* 15
                                 3700h
        3fØØh
                         4
* 17
        4100h
                         6
                                 3bØØh
                                 3fØØh
 2
        3200h
* 4
        3400h
* 6
        3600h
* 3
        38ØØh
* 1Ø
        3aØØh
* 12
        3cØØh
* 14
        3eØØh
* 16
        4000h
diff
                 80h-$
        set
        lxi
                 sp,wstk256+diff ;Set up stack at end of this sector
                 b,17*100h+1
                                  ;B = sector count, C = sector #
        lxi
wlod256 push
                                  ;Save sector and count
                 b
        call
                                  ; Set the next sector to read
                 djsec
        lxi
                 h,ccp+300h
                                  :Get DMA address (self modifying)
wdma256 equ
                 $-2
                                  :Storage for previous DMA address
        lxi
                 d, 200h
                                  ;Offset to new DMA address
        dad
                                  ; Add in offset, HL = new DMA address
                 d
        shld
                 wdma256+diff
                                  ;Save new DMA address
                                  ; Put DMA address into BC
        mov
                 b, h
        mov
                 c,1
        call
                 djdma
                                  ;Set the DMA address
                 wrd256+diff
        call
                                  ;Attempt a read
        pop
                 b
                                  ; Recover sector number and count
                                          B = count, C = number
                                  ; Update the sector count
        dcr
                 b
        jz
                 bios+3
                                  ;All done ?
                                  :Sector update
        mvi
                 a,2
        add
                                  ; Add in the sector skew factor
                 C
                                  ; Put new sector back into C
        mov
                 c,a
                 19
                                  ; Past the end of the track?
        cpi
        jc
                 wlod256+diff
                                  :Take jump if not past end of track
                 17
                                  ; Perform a negative sector adjustment
        sui
                                  ; Put new sector in C
        mov
                 c,a
        lxi
                 h,ccp+400h
                                  ; Negative DMA adjustment
                                  ; Save the new DMA address
        shld
                 wdma256+diff
         jmp
                 wlod256+diff
                                  ;Continue reading
* Wrd256 does the actual read from the controller, the DMA
  address and sector # have already been set up.
wrd256
        lxi
                 b,retries*100h+1 ;Maximum # of attempts
wr256
                 b
                                  ;Save error count
        push
                                  ; Initialize the track
        call
                 djtrk
         call
                 djread
                                  ;Attempt the read
                                  ; Restore the error count
                 b
         pop
                                  :Return if no error
         rnc
                                  ; Update error count
         dcr
         jnz
                 wr256+diff
                                  ;Try again if not to many errors
                                  ; Go and flash the light on controller
         jmp
                 djerr
                 89h-($ mod 89h)
        ds
```

we+k256 equ

```
Disk Jockey 2D CP/M from a 512 byte sector diskette.
diff
                80h-$
        set
        lxi
                sp, wstk512+diff ; Set up stack at end of this sector
        lxi
                b,1*100h+9
                                 ;B = sector count, C = sector #
        call
                wlod512+diff
                                 ;Load sector 9 into CCP
        lxi
                                 ; Destination of move
                h,ccp+1500h
        lxi
                d,ccp+500h
                                 ;Source of move
        mvi
                c,Ø
mov512 ldax
                d
                                 ;Get a byte of source
                                 ; Move it
        mov
                m,a
        inx
                h
                                 ;Bump destination
        inx
                đ
                                 ;Bump source
        dcr
                C
                                 ;All done with this page?
        inz
                mov512+diff
        lxi
                h,ccp+300h
                                 ;Initial DMA address
        shld
                wdma512+diff
        lxi
                b,8*100h+2
                                 ;B = sector count, C = sector #
                wlod512+diff
        call
                bios+3
        jmp
wlod512 push
                                 ;Save sector and count
                b
        call
                                 ; Set the next sector to read
                djsec
        lxi
                h,ccp+100h
                                 ;Get DMA address (self modifying)
wdma512 equ
                $-2
                                 ;Storage for previous DMA address
        lxi
                d.400h
                                 ;Offset to new DMA address
        dad
                                 ; Add in offset, HL = new DMA address
        shld
                wdma512+diff
                                 ;Save new DMA address
                b,h
                                 ; Put DMA address into BC
        mov
        mov
                c,1
        call
                djdma
                                 ;Set the DMA address
                wrd512+diff
        call
                                 ;Attempt a read
                                 ; Recover sector number and count
        pop
                b
                                         B = count, C = number
                                 ; Update sector count
        dcr
                 b
                                 ;All done ?
        rz
        mvi
                a,2
                                 :Sector update
        add
                 C
                                 ; Add in the sector skew factor
        mov
                                 ; Put new sector back into C
                 c.a
                1Ø
        cpi
                                 ; Past the end of the track?
        jc
                wlod512+diff
                                 ; Take jump if not past end of track
        sui
                                 ; Perform a negative sector adjustment
        mov
                                 ; Put new sector in C
                c,a
        lxi
                h,ccp+100h
                                 ; Negative DMA adjustment
        shld
                wdma512+diff
                                 ; Save the new DMA address
        jmp
                 wlod512+diff
                                 ;Continue reading
  Wrd512 does the actual read from the controller, the DMA
  address and sector # have already been set up.
wrd512 lxi
                 b, retries*109h+1 ; Maximum # of attempts
wr512
        push
                b
                                 ;Save error count
        call
                 djtrk
                                 ; Initialize the track
        call
                 djread
                                 ;Attempt the read
                                 :Restore the error count
        qoq
                 b
                                 ;Return if no error
        rnc
```

*Hodate error count

der

```
jnz
                wr512+diff
                                 ;Try again if not to many errors
        jmp
                djerr
                                 ; Go and flash the light on controller
        ds
                8Øh-($ mod 8Øh)
wstk512 equ
 The next sector loads CP/M from a 1024 byte sector diskette.
diff
        set
                8Øh-$
        lxi
                sp, wstkl24+diff ;Set up stack at end of this sector
        lxi
                b,1*100h+5
                                 ;B = sector count, C = sector #
        call
                wlod124+diff
                                 ;Load sector 6 into CCP
        lxi
                h,ccp+1500h
                                 :Destination of move
        lxi
                d,ccp+500h
                                 ;Source of move
        mvi
                c,Ø
mov124
        ldax
                đ
                                 ;Get a byte of source
        mov
                                 ; Move it
        inx
                                 ;Bump destination
                h
        inx
                d
                                 ;Bump source
        dcr
                С
                                 ;All done with this page?
        jnz
                mov124+diff
        lxi
                h,ccp+100h
                                 ;Initial DMA address
        shld
                wdma124+diff
        lxi
                b,4*100h+2
                                 ;B = sector count, C = sector #
        call
                wlod124+diff
        jmp
                bios+3
wlod124 push
                b
                                 ;Save sector and count
        call
                djsec
                                 ; Set the next sector to read
        lxi
                h,ccp-300h
                                 ;Get DMA address (self modifying)
wdma124 equ
                $-2
                                 ;Storage for previous DMA address
        lxi
                d,800h
                                 ;Offset to new DMA address
        dad
                                 ; Add in offset, HL = new DMA address
        shld
                wdmal24+diff
                                 ;Save new DMA address
        mov
                b,h
                                 ; Put DMA address into BC
        mov
                c,1
        call
                djdma
                                 ;Set the DMA address
        call
                wrd124+diff
                                 ;Attempt a read
        pop
                                 ; Recover sector number and count
                                         B = count, C = number
        dcr
                b
                                 ; Update sector count
        rz
                                 ;All done ?
        mvi
                a,2
                                 ;Sector update
        add
                C
                                 ;Add in the sector skew factor
        mov
                                 ; Put new sector back into C
                c,a
        cpi
                6
                                 ; Past the end of the track ?
        jс
                wlod124+diff
                                 ; Take jump if not past end of track
        sui
                                 ; Perform a negative sector adjustment
        mov
                                 ; Put new sector in C
                c,a
        lxi
                h,ccp-300h
                                 ; Negative DMA adjustment
        shld
                wdma124+diff
                                 ; Save the new DMA address
        jmp
                wlod124+diff
                                 ;Continue reading
* Wrl24 does the actual read from the controller, the DMA
 address and sector # have already been set up.
wrdl24 lxi
                b,retries*100h+1 ;Maximum # of attempts
wr124 push
                                 ·Save error count
```

```
call
                djtrk
                                 ; Initialize the track
        call
                                 ;Attempt the read
                djread
                                 ; Restore the error count
        pop
                b
        rnc
                                 Return if no error
        dcr
                                 ; Update error count
                b
        jnz
                wrl24+diff
                                 ;Try again if not to many errors
                                 ;Go and flash the light on controller
        jmp
                djerr
        ds
                80h-($ mod 30h)
wstkl24 equ
* The next sector of code resides at CCP+500h. It's task is to
* move the firmware code into the Disk Jockey Ram, then
* loading a sector into 80H which will load the rest of CP/M.
 The sector loaded at 80H is dependent on the sector size
 of the diskette being booted from.
diff
                ccp+500h-$
                                 ;Used to relocate this sector of code
        set
        jmp
                docold+diff
                                 ;Jump to cold boot portion
                                 ; Jump to warm boot portion
                dowarm+diff
        jmp
docold lxi
                 sp,stk+diff
                                 ;Set up initial stack at end of this sector
        lxi
                h,djram
                                 ;Destination pointer
        lxi
                d,stk+diff
                                 ;Source pointer
        lxi
                b,300h
                                 ;Length of transfer
        ldax
mloop
                đ
                                 ;Get one byte of source
        mov
                m,a
                                 ; Put at destination
        inx
                h
                                 ;Bump destination
        inx
                d
                                 ;Bump source
        dcx
                b
                                 ;Update count of bytes to move
                                 ;Test if all done
        mov
                a,b
        ora
        jnz
                mloop+diff
                                 ;Continue moving New Firmware
        call
                                 ; Initialize the new firmware
                diboot
        mvi
                a,l
        sta
                 botbias+diff
                                 ;Set up for proper sector select
                sp,stk+diff
dowarm
       lxi
        mvi
                 c,l
                                 ;Set the density to double
        call
                djden
        mvi
                 c,1
                                 ;Set up to read sector 1 on track 1
        mov
                 a,c
                 trknum+diff
        sta
                                 ;Set track
        call
                djsec
                                 ;Set sector
        lxi
                 b, stk+diff
                                 ;Set the DMA address
        call
                 djdma
        call
                 reed+diff
                                 ; Read the sector into memory at
                                         end of this sector
        call
                 djstat
                                 ;Determine the sector size
                                 ;Strip off unwanted bits
        ani
                 Øch
        rar
                                 ;Form the desired sector for Cold Boot
                                         based on the length of the
        rar
        adi
                 4
                                          sectors on this diskette
                 $-1
botbias equ
        mov
                 c,a
                                  ; Prepare to read the Cold Boot
        call
                 djsec
                                 ; Set up the sector
        xra
                                 ;Track Ø
                 trknum+diff
        sta
        lxi
                 b,89h
                                  ;Cold Boot loads at 80H
        nush
                                  ·Used as jump address to Cold Boot--
```

```
call
                djdma
        mvi
                c,Ø
                                 ; Density on track Ø is single
        call
                djden
        call
                reed+diff
                                 ; Read in the Cold Boot
        mvi
                c,1
                                 ;Set the density back to double
        jmp
                djden
                                 ;Go to the Cold Boot <-----
  Reed does the actual read from the controller, the DMA
  address and sector # have already been set up.
reed
                b, retries*100h+0; Maximum # of attempts
trknum
        equ
                $-2
                                 ;Storage for track number
reedl
        push
                b
                                 ; Save error count
        call
                djtrk
                                 ;Initialize the track
        call
                djread
                                 ;Attempt the read
        pop
                b
                                 ; Restore the error count
        rnc
                                 ; Return if no error
        dcr
                b
                                 ; Update error count
        jnz
                reedl+diff
                                 ;Try again if not to many errors
        jmp
                djerr
                                 ;Go and flash the light on controller
        ds
                80h-($ mod 80h)
stk
        equ
        else
 Cold Boot loader for Discus M10, M20, M26.
        if
                maxflop ne Ø
        org
                origin
                                 ;Org program at Floppy origin
        else
        org
                100h
                                 ; or else at 100h
        endif
diff
        set
                100h-$
boothd
       lxi
                sp,cstkhd+diff ;Set up stack at end of this sector
        lxi
                b,1*100h+20
                                 ;B = sector count, C = sector #
        call
                clodhd+diff
                                 ;Load sector 20 into CCP
        lxi
                h,ccp+le00h
                                 ;Destination of move
        lxi
                d,ccp
                                 ;Source of move
        mvi
                c,Ø
cmovhd
        ldax
                d
                                 ;Get a byte of source
        mov
                m,a
                                 ; Move it
        inx
                h
                                 ;Bump destination
        inx
                                 ;Bump source
        dcr
                                 ;All done with this page ?
        inz
                cmovhd+diff
        lxi
                h,ccp-200h
                                 ;Initial DMA address
        shld
                cdmahd+diff
        lxi
                b,15*100h+5
                                 ;B = sector count, C = sector #
        call
                clodhd+diff
        if
                maxflop ne Ø
        mvi
                c,17
                                 ; Check if Discus 2D is present
        mvi
                a,(jmp)
                                 ; Should be 17 jumps in the jump table
        lxi
                h, origin
clop
        cmp
        jnz
                bios
                                 :Not 17 jumps, don't read in the ramware
```

```
inx
                h
                                 ; Skip over the jump instruction
        inx
                h
        inx
        dcr
                                  ;Update jump counter
                С
                clop+diff
        jnz
                                  ; Continue checking
        lxi
                h,djram-200h
                                  ;Load in the Disk Jockey 2D ramware
        shld
                cdmahd+diff
        lxi
                b,2*100h+3
                clodhd+diff
        call
        endif
        jmp
                bios
                                 ; Go to CP/M
clodhd
                b
                                  ; Save sector and count
        push
        mov
                a,c
                hdsec+diff
        sta
                h,ccp-200h
        1xi
                                  ;Get DMA address (self modifying)
cdmahd
        equ
                $-2
                                  :Storage for previous DMA address
        lxi
                d,200h
                                  ;Offset to new DMA address
                                  ; Add in offset, HL = new DMA address
        dad
                 cdmahd+diff
        shld
                                  ;Save new DMA address
        call
                 crdhd+diff
                                  ;Attempt a read
        pop
                                  ; Recover sector number and count
                                          B = count, C = number
                                 ; Update sector count
        dcr
                                  ;All done ?
        rz
        inr
                 C
                 clodhd+diff
        jmp
                                  ;Continue reading
 Rdhd does the actual read from the controller, the DMA
  address and sector # have already been set up.
                 b,retries*100h+1 ;Maximum # of attempts
crdhd
        lxi
crhd
                                  ;Save error count
        push
                 hdread+diff
                                  ;Attempt the read
        call
        pop
                                  ; Restore the error count
                                  ; Return if no error
        rnc
        dcr
                 b
                                  ;Update error count
                 crhd+diff
                                  ;Try again if not to many errors
        jnz
        jmp
                                  ;Dynamic error halt
hdread
        call
                 hdprep+diff
                                 ;Prepare the sector header image
        rc
                                  ;Error exit
        mvi
                 a, rsect
                                  ; Read sector command
        out
                 hdcmnd
                 process+diff
        call
                                  ; Process the read
                                  :Error exit
        rc
        xra
                                  ; Pointer to data buffer
                 a
                 hdcmnd
        out
        mvi
                 b, secln/4
                                  ; Number of bytes to read
                 cdmahd+diff
        lhld
                                  Get destination of data
        in
                 hddata
                                  ;Two dummy data bytes
                 hddata
        in
rtloop
                 hddata
        in
                                  ;Move four bytes
                                  ;Byte one
        mov
                 m,a
        inx
                 h
        in
                 hddata
                                  ;Byte two
        mov
                 m,a
        inx
                 h
        in
                 hddata
                                  ;Byte three
        mov
                 m,a
        inx
                 h
        in
                 hddata
                                  ;Byte four
        mosz
```

```
inx
                h
        dcr
                                 ;Update byte count
        jnz
                rtloop+diff
        ret
process in
                hdstat
                                 ;Wait for command to finish
        mov
                b,a
        ani
                opdone
        jz
                process+diff
        mvi
                a, dskclk
                                 ;Turn on Disk Clock
                hdcntl
        out
        in
                hdstat
                tmout
        ani
                                 ;Timed out ?
        stc
        rnz
        in
                hdreslt
        ani
                retry
                                 ;Any retries ?
        stc
        rnz
        xra
                a
                                 ;No error exit
        ret
hdprep
        in
                hdstat
                                 ; Is Drive ready?
                drvrdy
        ani
        stc
        rnz
        mvi
                a, isbuff
                                 ; Initialize pointer to header buffer
                hdcmnd
        out
                a, null
        mvi
        out
                hdfunc
                                 ;Select drive A
        xra
        out
                hddata
                                 ; Form head byte
                hddata
        out
                                 ; Form track byte
        mvi
                a,Ø
                                 ;Form sector byte
hdsec
                $-1
        equ
                hddata
        out
        mvi
                a,80h
                                 ;Form Key
                hddata
        out
                a,dskclk
        mvi
                                 ;Turn on Disk clock
        out
                hdcntl
                a, wenabl
        mvi
                                 ;Write enable on
        out
                hdcntl
        ret
        org
                boothd+200h-2
cstkhd
        equ
        dw
                boothd+diff
                200h
        ds
        endif
* Disk Jockey 2D firmware revision 3.1 and 4.0 \,
* By George Morrow
* The following firmware is loaded into memory and then moved
  into the controller ram.
        if
                maxflop ne Ø
        equ
                origin
rom
ram
        equ
                origin+400h
IO
        EOU
                 ROM+3f8h
```

```
IO
UDATA
        EQU
DCMD
        EQU
                10+1
                 DCMD
DSTAT
        EQU
DREG
        EQU
                 IO+2
USTAT
                DREG
        EQU
CMDREG
        EQU
                IO+4
CSTAT
        EQU
                CMDREG
TRKREG
        EQU
                IO+5
SECREG
        EQU
                 IO+6
DATREG
        EQU
                 IO+7
RCMD
        EQU
                 200Q
                 240Q
WCMD
        EQU
HEAD
        EQU
                 2ØQ
LOAD
        EQU
DENSTY
        EQU
                 1
ULOAD
        EQU
                 3ØQ
RSTBIT
        EQU
                 4
ACCESS
                 2
        EQU
READY
        EQU
                 40Q
INDEX
                 2ØQ
        EQU
RACMD
        EQU
                 3Ø4Q
CLRCMD
        EQU
                 32ØQ
SVCMD
        EQU
                 35Q
SKCMD
                 3ØQ
        EQU
HCMD
                 110
        EQU
ISTAT
        EQU
                 4
OSTAT
        EQU
                 100
DSIDE
        EQU
                 1ØQ
TZERO
        EQU
                 4
                 3
MDINT
        EQU
LIGHT
        EQU
                 36Q
NOLITE EQU
                 76Q
                 BOOT
DBOOT
        JMP
TERMIN
        JMP
                 origin+3
TRMOUT
        JMP
                 origin+6
TKZERO
        JMP
                 HOME
TRKSET
        JMP
                 SEEK
SETSEC
                 SECSET
        JMP
SETDMA
        JMP
                 DMA
DREAD
        JMP
                 READ
DWRITE
        JMP
                 WRITE
SELDRV
        JMP
                 DRIVE
TPANIC
        JMP
                 origin+leh
TSTAT
        JMP
                 origin+21h
DMAST
                 DMSTAT
        JMP
STATUS
        JMP
                 DISKST
DSKERR
        JMP
                 ROM+52Q
        JMP
SETDEN
                 DENFIX
SETSID
        JMP
                 SIDEFX
BOOT
         mov
                 a,c
                 disk
         sta
                 bdisk
         sta
                 a,7fh
         mvi
dsrt
         rlc
         dcr
                 C
        jр
                 dsrt
                 drvsel
         sta
         sta
                 bdrvsel
         if
                 first
                 fixio
         call
```

```
endif
        lda
                io-4
                                 ;Test for Model A or B
                (ret)
        cpi
        jz
                modela
        lxi
                d, origin
        lxi
                h,ram
        mvi
                c,boot-dboot
                                 ;Copy prom jump table into ram
        if
                first
        call
                modelm
                a,7fh
        mvi
        call
                sdsel
        mvi
                a,clrcmd
        sta
                cmdreg
        lda
                bdrvsel
        ori
                60h
                7fh
        ani
        call
                sdsel
        mvi
                a,9h
        call
                 scbits
                a,(jz)
        mvi
        sta
                indxl
        sta
                indx3
        mvi
                 a,(jnz)
                indx2
        sta
        call
                measur
        xchq
        shld
                btimer
        lxi
                 d,btble
        lxi
                h,origin+7e3h
        mvi
                 c,23
        endif
        ldax
                 d
modelm
        mov
                 m,a
        inx
                 d
        inx
                 h
        dcr
                 C
        jnz
                 modelm
        ret
btble
        db
btimer
                 1800h,0
        dw
                 3.0
        db
bdrvsel db
                 7eh
bdisk
                 Ø,8,Ø,9,Øffh,9,Øffh,9,Øffh,9,Ø,1,Ø
fixio
        LHLD
                 ROM+7
                                  ; find the 2nd
        INX
                 H
                                     byte of input routine
        LXI
                 D, 4
                                  ;offset
                                  ;get addr of USTAT
        MOV
                 A, M
                                  ;I/O routines
        LXI
                 H, SDSEL+1
        MOV
                                  ;store USTAT addr
                 M,A
        DAD
                                  ;increment mem addr
                 D
        MOV
                 M, A
                                  ;store USTAT addr
                                  ;increment mem addr
        DAD
                 D
                 3
        XRI
                                  ;switch the addr
                                  ;store DSTAT addr
        VOM
                 M,A
                                  ;increment mem addr
        DAD
                 D
                                  ;store DATAT addr
        MOV
                 M, A
        DAD
                                  ;increment mem addr
                                  ;store DSTAT addr
        VOM
                 M, A
        ret
modela
       equ
        iĒ
                 not first
                 c,Ø
        mvi
                                  ;Copy last page of ram
                 modelm
        call
```

```
endif
        call
                fixio
                a,3
        mvi
        call
                scbits
        MVI
                A, CLRCMD
                                 ;1791 reset
                                 ; command
        STA
                CMDREG
        lda
                drvsel
                                 ;initialize 1791
        call
                sdsel
                                      control bits
        call
                measur
        xchq
        SHLD
                TIMER
        RET
DISKST
        LDA
                SECREG
                                 ;get current
        MOV
                                 ; sector no in B
                B, A
        LDA
                TRKREG
                                 ;get current
        MOV
                C,A
                                 ; track no in C
        LDA
                DCREG
                                 ;get current
        CMA
                                     density in
                1
        ANI
                                     the msb
                                 ;position
        RRC
        MOV
                D, A
                                 ;save in D
        LDA
                SIDE
                                 ; put the
        RLC
                                     side
        RLC
                                     select
        RLC
                                     flag
        ADD
                D
                                     in bit
        MOV
                D, A
                                     position 6
        LDA
                SECLEN
                                 ; put the
        RLC
                                     sector length
        RLC
                                     code P bits
        ADD
                D
                                     2 & 3
        VOM
                D,A
        LDA
                CDISK
                                 ; put the current
        ADD
                                     disk no in bits
        RET
                                     Ø & 1
DMSTAT
        PUSH
                H
                                 ;save the H-L pair
        LHLD
                DMAADR
                                 ;H-L pain
        MOV
                B,H
                                 ; move the DMA
        VOM
                C,L
                                 ;addr to B-C
        POP
                                 ;recover H-L
        RET
DRIVE
                A,374Q
        IVM
                                 ;test for the
        ADD
                                 ; new drive number
        IVM
                A,20Q
                                 ; less than 4
        RC
        MOV
                A,C
                                 ;store the new
        STA
                DISK
                                 ;drive in DISK
        RET
DMA
        LXI
                H, 8-ROM
                                 ;test the
        DAD
                                     DMA address
        JNC
                DMASET
                                     for conflict
        LXI
                H,-RAM
                                     with the I/O
        DAD
                В
                                     on the DJ/2D
```

.TC

DMASET

•controller

```
STC
        MVI
                A,20Q
        RET
DMASET
        MOV
                H,B
                                 ;get the DMA addr
                L,C
        VOM
                                 ;to the H-L par
        SHLD
                DMAADR
                                 ;store
        XRA
                                 ; clear the error
                Α
        RET
                                     flag and return
HOME
        call
                hdload
                                 ; load the head
        rc
                                 ;not ready error
        call
                hentry
                                 ; move the head
        PUSH
                PSW
                                 ; save the flags
        SBB
                A
                                 ;update the
        STA
                TRACK
                                 ; track
                                 ; registers
        sta
                trkreg
                                 ; set the not
        xra
                a
        sta
                tzflag
                                 ; verified flag
                                 ;unload the head
        JMP
                LEAVE+1
HENTRY
        XRA
                A
                                 ;update
        STA
                HDFLAG
                                 ; flags
        LXI
                H,Ø
                                 ;time out constant
                                 :do the home
        MVI
                A, HCMD
        CALL
                CENTRY
                                     command
        ANI
                TZERO
                                 ;track zero bit
        RNZ
        STC
                                 ;error flag
        RET
SECSET
        XRA
                Α
                                 ;test for
        ORA
                C
                                 ; sector zero
        STC
                                 ;error flag
        RZ
        MOV
                A,C
                                 ;test for
        CPI
                27
                                 ; sector
        CMC
                                 ;too large
        RC
        STA
                 SECTOR
                                 ;save
        RET
SEEK
        MOV
                                  ;test for
                A,C
        CPI
                                     track
                 77
        CMC
                                     too large
        RC
        STA
                 TRACK
                                  ;save
        RET
issue
                 ecount+1
                                  ;update count
        sta
                                  ; find the index
        call
                measur
                                  ;start w/sector 1
        mvi
                 c,1
isloop
        mov
                 a,c
                                  ; initialize the
                                 ; sector register
        sta
                 secreg
        lda
                                 ;test for
                 sector
        cmp
                                  ; target sector
                 C
```

r 2

```
a,rcmd
                                  ;do a fake
        mvi
        call
                 comand
                                  ; read command
        jc
                 pleave
                                  ;abort on error
                                  ; increment sector no.
        inr
                 C
        jmp
                 isloop
comndp
        sta
                 cmdreg
                                  ; do the command
                                  ; initialize block count
        mov
                 c,b
        lxi
                 d,datreq
                                  ;data register
                                  ;transfer address
        lhld
                 dmaadr
        ret
write
        CALL
                 PREP
                                  ;prepare for write
        jс
                 leave
                                  ; abort operation
wrentry
        mvi
                 a, wcmd
                                  ;start a write
                                  ; sector operation
        call
                 comndp
wrloop
        mov
                 a,m
                                  ;load 1st byte of data
        inx
                 h
                                  ; advance pointer
        stax
                 d
                                  ;write 1st byte of data
                                  ;load 2nd byte of data
        mov
                 a,m
        inx
                 h
                                  ; advance pointer
                                  ;write 2nd byte of data
                 đ
        stax
        mov
                                  ;load 3rd byte of data
                 a,m
                                  ; advance pointer
        inx
                 h
                                  ;write 3rd byte of data
        stax
                 đ
                                  ; reduce block count
        dcr
                 С
        mov
                                  ;load 4th byte of data
                 a,m
        inx
                 h
                                  ; advance pointer
        stax
                                  ;write 4th byte of data
        jnz
                 wrloop
                                  ;write next 4 bytes
        lxi
                 h, wrentry
                                  ;return entry address
        jmp
                 cbusy
read
        call
                 prep
                                  ;prepare for read
         jс
                 leave
                                  ;abort operation
rdentry
        mvi
                 a,rcmd
                                  ;start a read
        call
                 comndp
                                  ; sector operation
rdloop
        ldax
                 đ
                                  ;read 1st byte
        mov
                                  ;store 1st byte
                 m,a
        inx
                 h
                                  ;advance pointer
        1dax
                 đ
                                  ;read 2nd byte
        mov
                                  ;store 2nd byte
                 m, a
        inx
                 h
                                  ; advance pointer
        ldax
                 đ
                                  ;read 3rd byte
                                  ;store 3rd byte
        mov
                 m,a
        inx
                                  ;advance pointer
                 h
        dcr
                                  ; reduce block count
                 C
        ldax
                 đ
                                  ;read 4th byte
                                  ;store 4th byte
        mov
                 m,a
        inx
                 h
                                  ;advance pointer
                                   ;read next 4 bytes
         jnz
                 rdloop
         lxi
                 h,rdentry
                                  ;return entry address
CBUSY
         push
                                   ;save return address
                 h
                                   ; wait for the 1791
         lxi
                 h,cstat
         call
                 busy
                                  ; to finish command
                                  ;error bit mask
         ani
                 137Q
         jz
                 leave-1
                                  ; test
        cni
                 10h
                                   •premature interrupt
```

```
jnz
                 pleave
                                  ;other error type
        lda
                 ecount
                                  ;decrement error
        dcr
                                  ; count number 1
        jm
                 stest
                                  ; hard interrupt error
        sta
                 ecount
                                  ;update count
        ret
                                  ;do operation over
stest
        1da
                 ecount+1
                                  ;decrement error
        dcr
                                  ; count number 2
                issue
        jр
                                  ;issue a command
        mvi
                a,10h
                                  ; irrecoverable error
pleave
        stc
                                  ;error flag
                                  ; adjust the stack
        pop
                h
LEAVE
        PUSH
                 PSW
                                  ; save the flags
        LDA
                 DCREG
                                  ;1791 control bits
        XRI
                 LOAD
                                  ;toggle the
        CALL
                 SCBITS
                                  ; head load bits
        POP
                 PSW
                                  ;recover the flags
        RET
PREP
        CALL
                 HDLOAD
                                  ;load the head
        RC
                                  ; disk not ready?
        LDA
                 TRKREG
                                  ; get the old trk
        INR
                 Α
                                  ;test for head
        CZ
                 HENTRY
                                  ; not calibrated
        rc
                                  ; seek error?
        LXI
                 H, TRKREG
                                  ;present trk
        LDA
                                  ; the new track
                 TRACK
        CMP
                 Μ
                                  ;test for head motion
        INX
                 Η
                                  ; advance to the
        INX
                 Η
                                      data register
        VOM
                 M,A
                                  ; save the new trk
        MOV
                 A,C
                                  ;turn off data
        CALL
                 SCBITS
                                      access control bit
        JZ
                 TVERFY
                                  ;test for seej
        XRA
                 Α
                                  ; force a read
        STA
                 HDFLAG
                                      header operation
        CALL
                 LDSTAT
                                  ; get the
        ANI
                 DSIDE
                                      double
        RAR
        RAR
                                      flag
        RAR
                                      to do 3 ms
        ADI
                 SKCMD
                                      step operation
        LXI
                 H,Ø
                                  ;do a seek
        CALL
                 CENTRY
                                  : command
        JC
                 SERROR
                                  ; seek error?
TVERFY
        LDA
                 HDFLAG
                                  ;get the force
        ORA
                 Α
                                  ; verify track flag
        JNZ
                 CHKSEC
                                  ;no seek & head OK
        MVI
                 B, 2
                                  ; verify retry no
SLOOP
        MVI
                 A, SVCMD
                                  ;do a verify
        CALL
                 COMAND
                                  ; command
        ANI
                 231Q
                                  ;error bit mask
                 d,a
        mov
                                  ;save
        JZ
                 RDHDR
                                  ;no error
        LDA
                 DCREG
                                  ;1791 control reg
        XRI
                 DENSTY
                                  ;flip the density bit
        STA
                 DCREG
                                  ;update
        XRI
                 ACCESS
        CALL
                 SCBITS
                                  :change density
```

```
DCR
                 В
                                  ; dec retry count
        JNZ
                 SLOOP
                                  ; and try again
                                  :restore error bits
                 a,d
        mov
SERROR
        stc
                                  ;error flag
        push
                                  ; save the status
                 psw
                                  ;seek to track Ø
        call
                 hentry
        pop
                 psw
                                  ;recover errors
        ret
RDHDR
        MVI
                 B,12Q
                                  ; number of retrys
RHLOOP
                 D, DATREG
        LXI
                                  ;data register
        LXI
                 H, TRACK+1
                                  ;storage area
        MVI
                 A, RACMD
                                  ; do the read
        STA
                                      header command
                 CMDREG
RHL1
        LDAX
                 D
                                  ;get a data byte
        MOV
                 M, A
                                  ;store in memory
        INR
                                  ;inc mem pointer
                 L
        JNZ
                                  ; test for more data
                 RHL1
        LXI
                 H, CSTAT
                                  ;wait for 1791
        CALL
                 BUSY
                                  ; to finish cmd
        ORA
                                  ;test for errors
                 Α
                                  ;transfer OK?
        JZ
                 CHKSEC
        DCR
                                  ;dec retry count
                                  :test for
        JNZ
                 RHLOOP
        JMP
                 SERROR
                                  ; hard error
CHKSEC
        LDA
                 SECLEN
                                  ; get the sector
        MOV
                 C,A
                                  ; size and setup
        IVM
                 B,\emptyset
                                  ; the offset
        LXI
                 H, STABLE
                                  ;sec size tbl
        DAD
                                  ; add the offset
                                  ;get the sector
        LDA
                 SECTOR
        MOV
                 B,A
                                  ;save in B
        ADD
                                  ; compare w/table entry
                 М
        IVM
                 A, 20Q
                                  ;error flag
        RC
                                  ;error return
        MOV
                 A,B
                                  ;save the sector
        STA
                 SECREG
                                  ; in sector reg
        mvi
                 a,40q
                                  ;128 byte sector
        lxi
                 h,505h
                                  ;initialize
         shld
                 ecount
                                  ; error counts
SZLOOP
         DCR
                 C
                                  ;reduce size count
        VOM
                 b,a
                                  ;sector size to b
        rm
                                  ;return on minus
        ral
                                  ; double the count
         ora
                                  ;clear the carry
                 a
                 SZLOOP
        JMP
SIDEFX
        MOV
                 A,C
                                  ; get the side bit
        ANI
                                  ;trim excess bits
         RAL
                                  ; move the bit
         RAL
                                       to the side
         RAL
                                       select bit
                                      position
         RAL
        STA
                 SIDE
                                  ;save
         RET
TOEND
```

300h-TOEND+DBOOT-660

```
DS
                 25Q
STACK
STABLE
        DB
                 345Q
        DB
                 345Q
        DB
                 36ØQ
                 367Q
        DB
ecount
                 Ø
        đw
                                  ;error count cells
TIMER
                 3000h
                                  ; head load time
DMAADR
        DW
                 2000
                                  ;dma address
HDFLAG
        DB
                 Ø
                                  ;read header flag
DRVSEL
        DB
                 3760
                                  ;drive select constant
DISK
        DB
                 Ø
                                  ; new drive
CDISK
        DB
                 1ØQ
                                  ; current drive
TZFLAG
        DB
                 Ø
                                  ;track zero indicator
DØPRAM
        DB
                 3
                                  ;drive Ø parameters
DØTRK
        DB
                 377Q
                                  ;drive Ø track no
DlPRAM
        DB
                 3
                                  ;drive 1 parameters
DITRK
        DB
                 3770
                                  ;drive 1 track no
D2PRAM
        DB
                 3
                                  ;drive 2 parameters
D2TRK
        DB
                 377Q
                                  ;drive 2 track no
D3PRAM
        DB
                 3
                                  ;drive 3 parameters
                 377Q
D3TRK
        DB
                                  ;drive 3 track no
DCREG
        DB
                 3
                                  ; current parameters
SIDE
        DB
                 Ø
                                  ; new side select
SECTOR
        DB
                 3
                                  ;new sector
        DB
                 Ø
TRACK
                                  ;new track
TRKNO
                 Ø
        DB
                                  ;disk
SIDENO
        DB
                 Ø
                                      sector
SECTNO
        DB
                 Ø
                                      header
                 Ø
SECLEN
        DB
                                      data
CRCLO
        DB
                 Ø
                                      buffer
                 Ø
CRCHI
        DB
HDLOAD
        LXI
                 H, DISK
        MOV
                 C,M
                                  ; new disk no to C
        INX
                 H
        MOV
                 E, M
                                  ; current disk to E
        VOM
                 M,C
                                  ;update current disk
        INX
                 H
                                  ;addr of disk table
        MOV
                 A,E
                                  ;test for
        CMP
                 C
                                      disk change
        MOV
                 A, M
                                  ; head load flag
        MVI
                 M, HEAD
                                  ;update head load
        INX
                                  ;addr of disk table
        JZ
                 HDCHK
                                  ;no disk change?
        PUSH
                 Η
                                  ; save table address
        MVI
                 D,Ø
                                  ;set up the
        VOM
                                      offset address
                 B, D
        DAD
                 D
                                  ;get the current
        DAD
                 D
                                      disk parameters
        LDA
                 DCREG
                                  ;save the
        MOV
                 M,A
                                  ;density info
        INX
                 H
                                  ; current track
        LXI
                 D, TRKREG
        LDAX
                 D
                                  ;get current trk
        VOM
                 M, A
                                  ;save
        POP
                                  recover the addr
```

```
; add the
        DAD
                 В
        DAD
                 В
                                      offset
        MOV
                 A,M
                                  ; get control bits
        STA
                 DCREG
                                  ;update DCREG
                                  ;get the old
        INX
                 H
        MOV
                 A, M
                                  ;track number
                                  ; and update 1791
        STAX
        MVI
                 A,177Q
                                  ; disk select bits
DSROT
        RLC
                                  ;rotate to
                 C
                                       select the
        DCR
        JΡ
                 DSROT
                                      proper drive
        STA
                 DRVSEL
                                  ;save
        XRA
                 Α
                                  ; force head load
HDCHK
        CALL
                 LOADS
                                   ;test for
        ANA
                 Μ
                                      head loaded
        STA
                 HDFLAG
                                   ;save the head
        PUSH
                 PSW
                                      loaded status
        LDA
                 DRVSEL
                                   ; get current drive
        VOM
                 C,A
                                   ; save
        LDA
                 SIDE
                                   ;get current side
        CMA
                                   ; and merge
        ANA
                 C
                                   ; with drive select
                                  ; select drive & side
        CALL
                 SDSEL
        LDA
                 DCREG
                                   ;1791 control bits
        MOV
                 C,A
                                   ; save
        LDA
                 TRACK
                                   ; get the new trk
        SUI
                 1
                                   ; force single
        SBB
                 Α
                                       density
        DCR
                                       if track = \emptyset
                 Α
        CMA
                                   ;compliment
                 C
        ORA
                                   ;merge w/control bits
        MOV
                 M, A
                                   ;set 1791 control
                                  ;toggel access bit
        XRI
                 ACCESS
        MOV
                                   ;save PREP routine
                 C,A
        POP
                 PSW
                                   ; head load status
        JNZ
                 RDYCHK
                                   ; conditionally
        PUSH
                 H
                                       wait for head
        LHLD
                 TIMER
                                       load time out
TLOOP
         DCX
                 H
                                   ;count down
        MOV
                                       40 ms for
                 A, H
        ORA
                 L
                                      head load
        JNZ
                 TLOOP
                                       time out
         POP
                 \mathbf{H}
                                   ; disk status addr
RDYCHK
        MOV
                 A, M
                                   ;test for
        ANI
                 READY
                                      disk ready
         RZ
UNLOAD
         LDA
                 DCREG
                                   ;force a
         ORI
                 ULOAD
                                       head
         MOV
                 M,A
                                       unload
         IVM
                                   ;set disk
                 A, 200Q
         STC
                                       not ready
         RET
                                       error flag
COMAND
         LHLD
                 TIMER
                                   ;get index count
         DAD
                 Η
                                       and multiply
         DAD
                 H
                                       by four
CENTRY
         XCHG
                                   ;save in D-E pair
                 H. CSTAT
         T.X.T
                                   •issue command
```

	MOV	M, A	;to the 1791
NBUSY	1100	11/11	, 60 che 1791
	VOM	A, M	;wait
	RAR	MOTOR	; for the
BUSY	JNC	NBUSY	; busy flag
5051	VOM	A, M.	;test for
	RAR		; device busy
	MOV	A,M	restore status
	RNC DCX	D	return if not busy
	MOV	D A,D	<pre>;test for ; two disk</pre>
	ORA	E E	; revolutions
	JNZ	BUSY	;47 machine cycles
	MOA	e,m	;save error code
	PUSH INX	H H	<pre>;save cmd address ;track register</pre>
	MOV	D, M	;save present track
	xthl		;recover cmd reg.
	push	đ	;save status
	xchg call	loads	<pre>;adjust registers ;get control reg</pre>
	LDA	DCREG	;1791 control bits
	xri	RSTBIT	;reset the 1791
	mov	m,a	; controller to
	xri xchq	rstbit	; clear fault
	stax	đ	;adjust registers ;start controller
	MVI	M, CLRCMD	; force an interrupt
	pop	đ	;recover status
	POP mov	H m,d	<pre>;recover track reg ;restore track</pre>
	MOA	a,e	;restore error code
	STC	-,-	; error flag
	RET		
*			
MEASUR			
	LXI	D,Ø	;initialize count
	CALL	LOADS	;status port
INDXHI	MVI	C, INDEX	;index bit flag
211111111111111111111111111111111111111	MOV	A, M	;wait for
	ANA	C	; index
indxl	JNZ	INDXHI	; pulse low
INDXLO	MOV	A,M	;wait for
	ANA	C	; index
indx2	JZ	INDXLO	; pulse high
INDXCT		_	_
	INX XTHL	D	;advance count ;four
	XTHL		; dummy
	XTHL		; instructions
	XTHL		; for delay
	MOV ANA	A,M C	;wait
indx3	JNZ	INDXCT	<pre>; for next ; low index</pre>
IIIdao	RET	***********	;98 machine cycles
*			•
* DENFIX			
DEMETY	VOM	A,C	trim excess;
	ANI	1	; bits,
	CMA		; compliment
	MOV	B.A	: B and save

```
;new disk
       LXI
                H, DISK
       VOM
                E,M
                                ; get disk no
       MVI
                D,Ø
                                ;offset addr
       INX
                                ; current disk
                H
       VOM
                A,M
                                ; move to ACC
                                ;compare w/new
       XRA
                E
       PUSH
                PSW
                                ;save status
       INX
                                ;disk table
                H
       INX
                H
                                ; address
       DAD
                D
                                ; add the
       DAD
                D
                                ;offset
                                ;get parameters
       MOV
                A,M
       ORI
                1
                                ; mask off density
       ANA
                В
                                ;set new density
        VOM
                M,A
                                ;update
        POP
                                ; check for nd=cd
                PSW
        RNZ
                                ;new disk not old
                                ;update CDISK
       MOV
                A,M
        STA
                DCREG
                                ; also
        RET
SDSEL
        STA
                DREG
                                ;drive select reg
        RET
LUSTAT
        LDA
                USTAT
                                ;UART status reg
        RET
SCBITS
        STA
                DCMD
                                ;1791 control reg
        RET
LDSTAT
        LDA
                DSTAT
                                ;drive status reg
        RET
LOADS
        LXI
                H, DSTAT
                                ;drive status reg
        RET
        endif
```

end